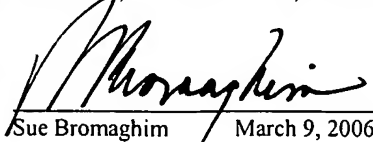




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Sue Bromaghim March 9, 2006

**CUSTOMER NO. 36257**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Jeffrey W. LUTZE et al.		
Title:	Self Aligned Non-Volatile Memory Cells and Processes for Fabrication		
Application No.:	10/799,060	Filing Date:	March 12, 2004
Examiner:	Mai Huong C. TRAN	Group Art Unit:	2818
Docket No.:	SNDK.352US0	Conf. No.:	6740

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicant(s) call(s) the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

- ☒ According to 37 C.F.R. 1.98(2)(ii), copies of the U.S. Patents and U.S. Published Patent Applications documents are not required and are therefore not enclosed. Copies of the listed foreign patent documents or Other Art are enclosed.

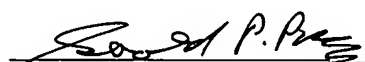
**Attorney Docket No.: SNDK.352US0**

**Application No.: 10/799,060**

Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or (3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and consequently no fee should be required. The Commissioner is authorized, however, to charge any fee that may be required, or to credit any overpayment, against Deposit Account No. 502664.

Respectfully submitted,

	<u>March 9, 2006</u>
Gerald P. Parsons	Date
Reg. No. 24,486	

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U.S. Department of Commerce, Patent and Trademark	Atty. Docket No.	Application No.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	SNDK.352US0	10/799,060
(Use several sheets if necessary)	Applicant(s)	Conf. No.
(Form PTO-1449)	Jeffrey W. LUTZE et al.	6740
	Filing Date	Art Group
	March 12, 2004	2818

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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
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Examiner

Date Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

U.S. Department of Commerce, Patent and Trademark				Atty. Docket No.			Application No.	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				SNDK.352US0			10/799,060	
				Applicant(s)			Conf. No.	
(Use several sheets if necessary)				Jeffrey W. LUTZE et al.			6740	
(Form PTO-1449)				Filing Date			Art Group	
				March 12, 2004			2818	
<b>U.S. Patent Documents</b>								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
<b>U.S. Published Patent Application Documents</b>								
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	25	2002/0048897 A1	04-2002	Hong				
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							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>								
	31	Park et al., "8Gb MLC (Multi-Level Cell) NAND Flash Memory Using 63 nm Process Technology", Samsung Electronics Co., Ltd., Semiconductor R & D Center, pp. 1-4.						
Examiner			Date Considered					
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>								